efficient usage of compute shaders on Xbox One and PS4

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#### GAME DEVELOPERS CONFERENCE EUROPE

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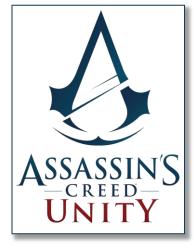






- Cloth simulation developed by Ubisoft
- Used in:













# Agenda

- What is this talk about?
- Why porting a cloth simulation to the GPU?
- The first attempts A new approach
- The shader Easy parts Complex parts
- Optimizing the shader
- The PS4 version
- What you can do & cannot do in compute shader
- Tips & tricks

# What is this talk about?

- Cloth simulation ported to the GPU
- For PC DirectX 11, Xbox One and PS4

# What is this talk about?

- Cloth simulation ported to the GPU
- For PC DirectX 11, Xbox One and PS4

 This talk is about all that we have learned during this adventure



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#### 5 ms of CPU time

	# of dancers
Xbox360	34





#### 5 ms of CPU time

	# of dancers
Xbox360	34
PS3	105





#### 5 ms of CPU time

	# of dancers
Xbox360	34
PS3	105

# Now let's switch to next gen!



#### 5 ms of CPU time

	# of dancers
Xbox360	34
PS3	105
PS4	98







#### 5 ms of CPU time

	# of dancers
Xbox360	34
PS3	105
PS4	98

5 SPUs @ 3.2 GHz



6 cores @ 1.6 GHz

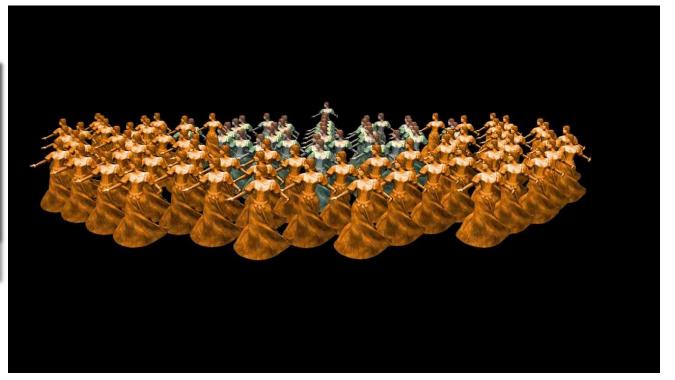






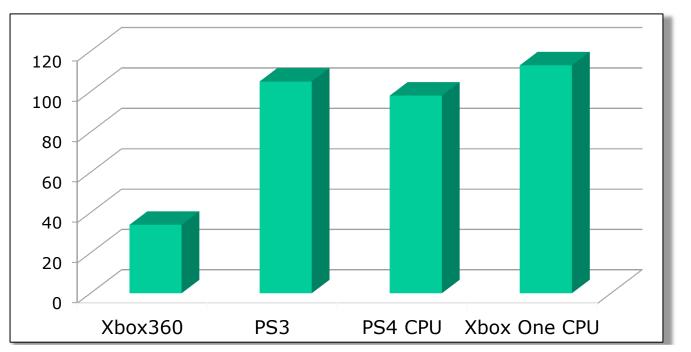
#### 5 ms of CPU time

	# of dancers
Xbox360	34
PS3	105
PS4	98
Xbox One	113





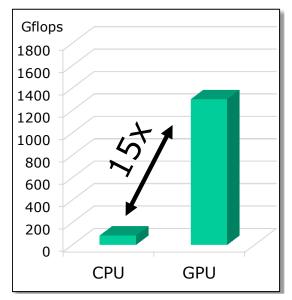
Next gen doesn't look sexy!



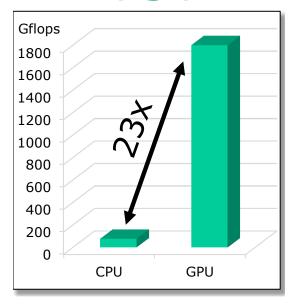
# What is the solution?



Peak power: Xbox One



PS4





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Not available on all platforms







Not available on all platforms





Black box: no possibility to know what's going on



# DirectCompute

Integrate velocity

Resolve some constraints

Resolve collisions

Resolve some more constraints

Do some other funny stuffs

. . .





Integrate velocity

Resolve some constraints

Resolve collisions

Resolve some more constraints

Do some other funny stuffs

Compute Shader

Compute Shader

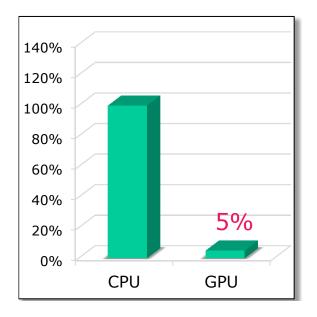
Compute Shader

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Compute Shader

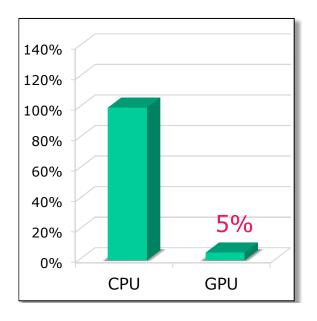
Compute Shader





Too many "Dispatch"





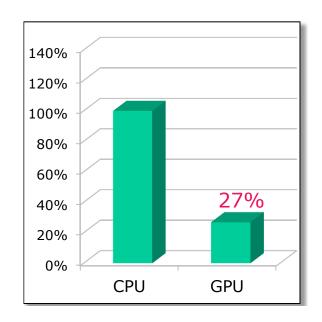
Too many "Dispatch"

Bottleneck = CPU



Merge several cloth items to get better performance

All cloth items must have the same properties





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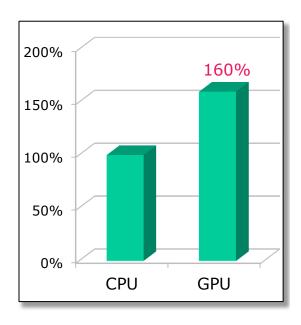
# A new approach

- A single huge compute shader to simulate the entire cloth
- Synchronization points inside the shader
- A single "Dispatch" instead of 50+



# A new approach

- A single huge compute shader to simulate the entire cloth
- Synchronization points inside the shader
- A single "Dispatch" instead of 50+
  - Simulate several cloth items (up to 32) using a single "Dispatch"



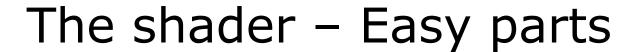


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# The shader

- 41 .hlsl files
- 3,100 lines of code
  - (+ 800 lines for unit tests & benchmarks)
- Compiled shader code size = 69 KB



Thread group:

We do the same operation on 64 vertices at a time



There must be no dependency between the threads

Read some global properties to apply (ex: gravity, wind)

Read position of vertex 0

Read position of vertex 1

. . .

Read position of vertex 63



Read some global properties to apply (ex: gravity, wind) Read position Read position Read position of vertex 0 of vertex 63 of vertex 1 Compute Compute Compute Write position Write position Write position of vertex 0 of vertex 1 of vertex 63



Read some global properties to apply (ex: gravity, wind) Read position Read position Read position of vertex 65 of vertex 64 of vertex 127 Compute Compute Compute Write position Write position Write position of vertex 64 of vertex 65 of vertex 127



Read position of vertex 0

Read position of vertex 1

- - -

Read position of vertex 63

Read property for vertex 0

Read property for vertex 1

- - -

Read property for vertex 63



# The shader – Easy parts

Read position of vertex 0

Read position of vertex 1

. . .

Read position of vertex 63

Read property for vertex 0

Read property for vertex 1

- - -

Read property for vertex 63

Compute

Compute

- - -

Compute

Write position of vertex 0

Write position of vertex 1

- - -

Write position of vertex 63

#### The shader – Easy parts

Read property for vertex 0 Read property for vertex 1 Read property

Ensure contiguous reads to get good performance

#### The shader – Easy parts

Read property for vertex 0

Read property for vertex 1

- - -

Read property for vertex 63



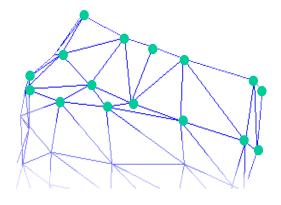
Ensure contiguous reads to get good performance



Coalescing = 1 read instead of 16

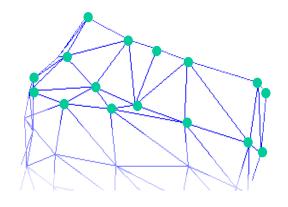
i.e. use Structure of Arrays (SoA) instead of Array of Structures (AoS)





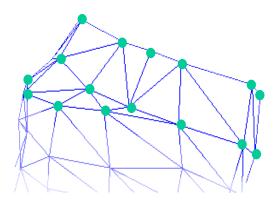






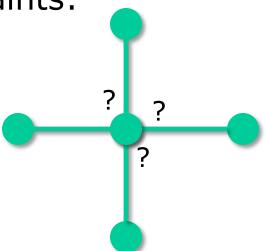


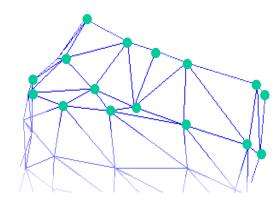




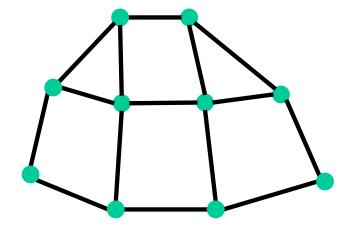








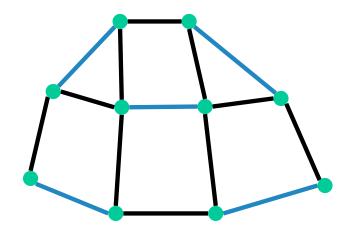






Binary constraints:

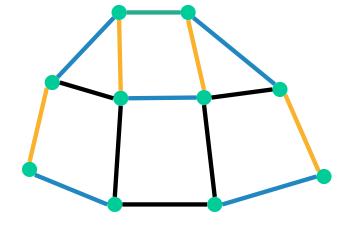
Group 1





Binary constraints:

Group 1



Group 2



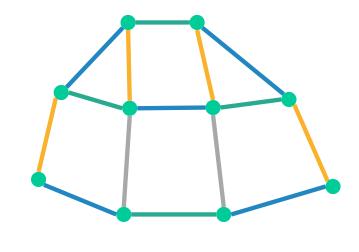
• Binary constraints: Group 1

Group 2

Group 3



Binary constraints:



Group 1

GroupMemoryBarrierWithGroupSync()

Group 2

GroupMemoryBarrierWithGroupSync()

Group 3

GroupMemoryBarrierWithGroupSync()

Group 4



- Collisions: Easy or not?
  - Collisions with vertices





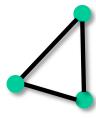
- Collisions: Easy or not?
  - Collisions with vertices



Collisions with triangles



Each thread will modify the position of 3 vertices





You have to create groups and add synchronization



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General rule:

Bottleneck = memory bandwidth

	CPU
Vertex	128 bits (4 floats)

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General rule:

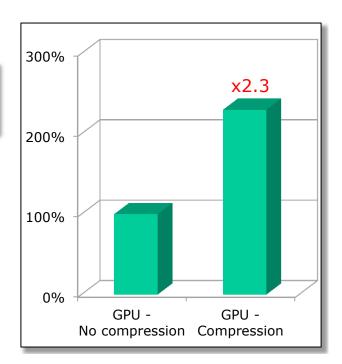
Bottleneck = memory bandwidth

	CPU	GPU
Vertex	128 bits (4 floats)	64 bits (21:21:1)
Normal	128 bits (4 floats)	

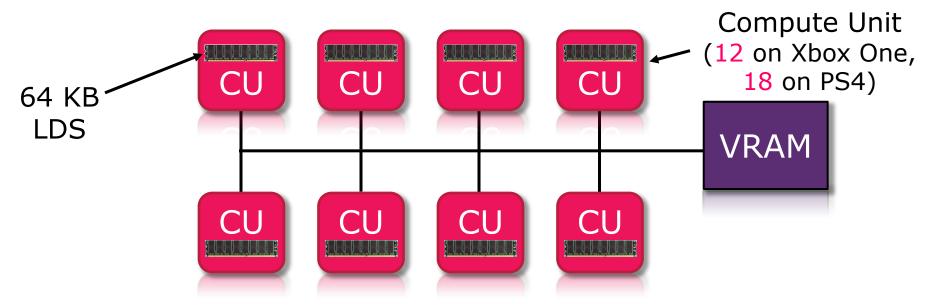
General rule:

Bottleneck = memory bandwidth

	CPU	GPU
Vertex	128 bits (4 floats)	64 bits (21:21:1)
Normal	128 bits (4 floats)	32 bits (10:10:10)



Use Local Data Storage (aka Local Shared Memory)



Store vertices in Local Data Storage

Copy vertices from VRAM to LDS



Store vertices in Local Data Storage

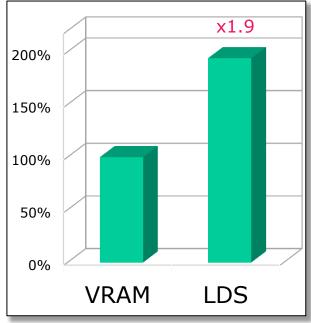
Copy vertices from VRAM to LDS

Step 1 – Update vertices

Step 2 – Update vertices

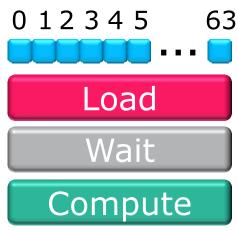
Step n – Update vertices

Copy vertices from LDS to VRAM



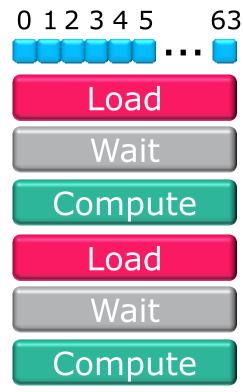


Use bigger thread groups



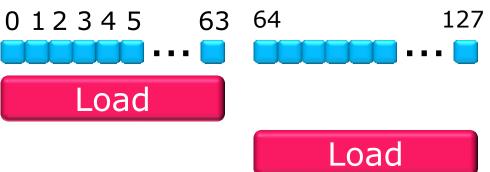


 Use bigger thread groups





Use bigger thread groups





 Use bigger thread groups 0 1 2 3 4 5 63 64 127
Load

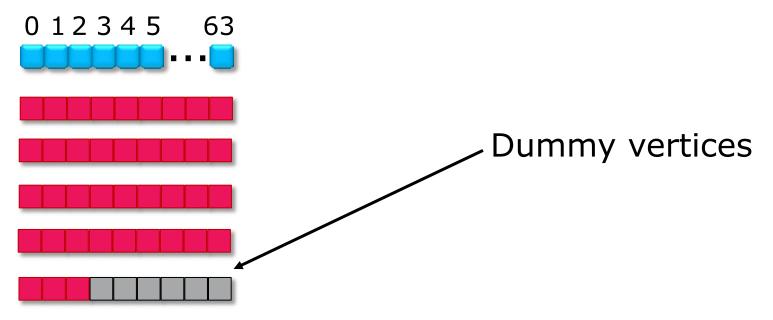
With 256 or 512 threads, we hide most of the latency!

Compute

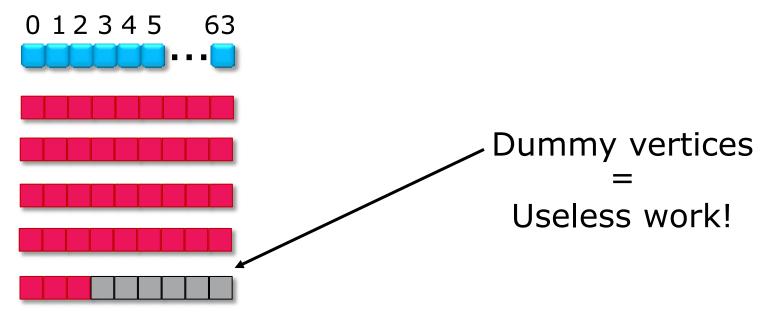
Compute

Load

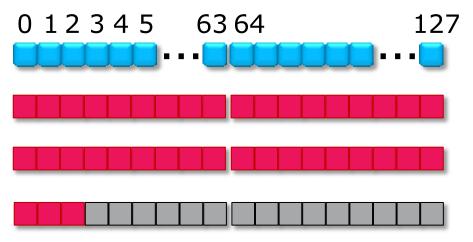




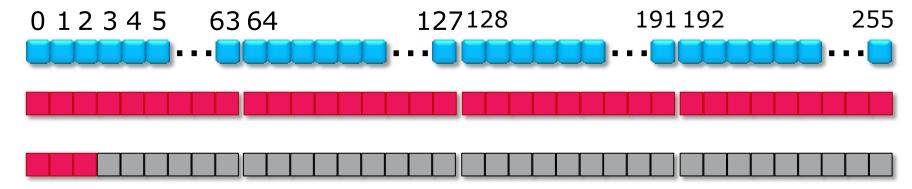




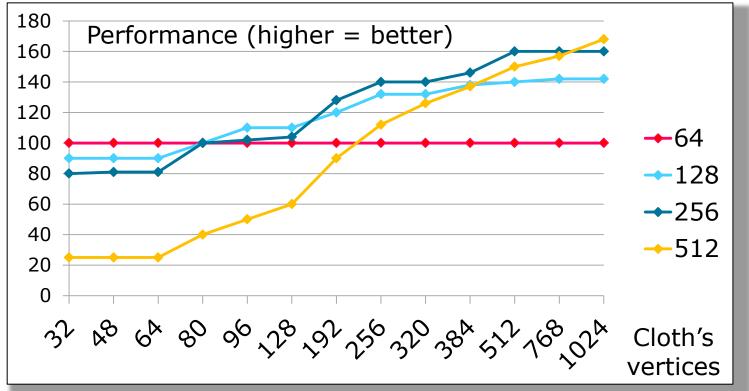




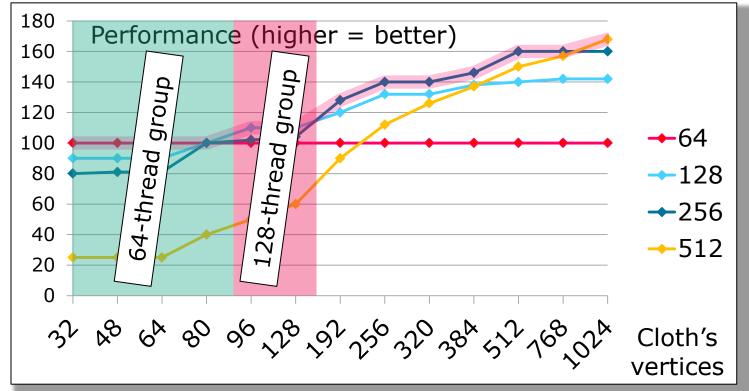




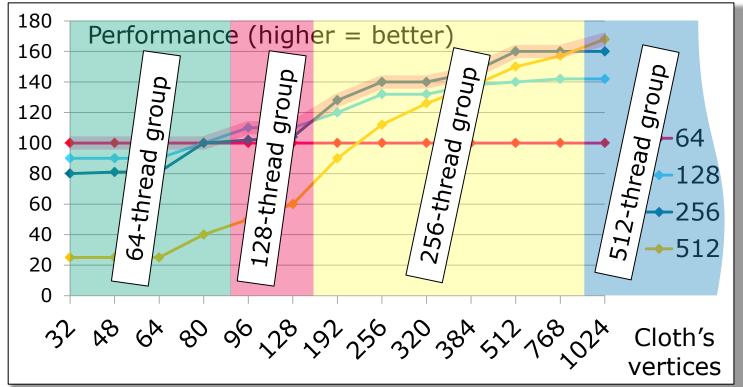














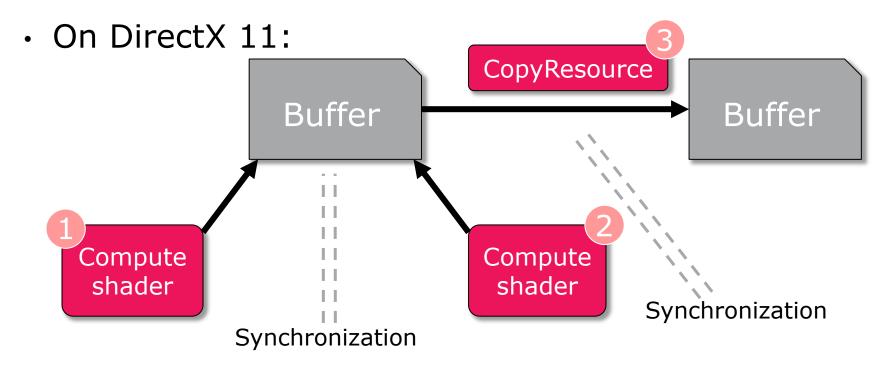
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#### The PS4 version

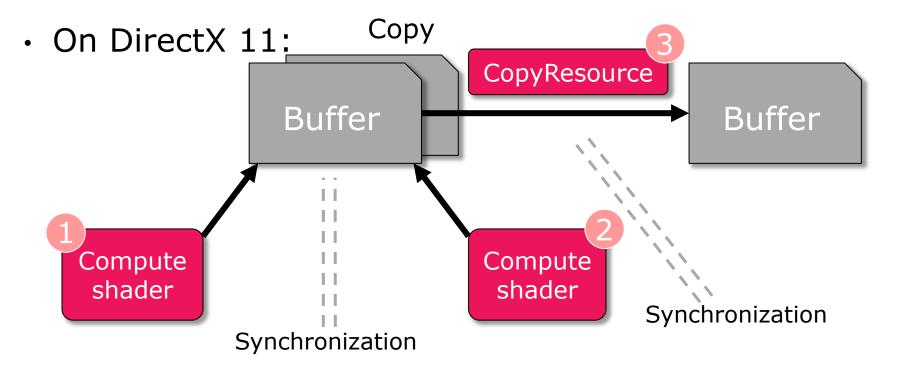
Port from HLSL to PSSL

```
#ifdef PSSL
  #define numthreads
                                           NUM THREADS
                                           S GROUP INDEX
  #define SV_GroupIndex
  #define SV GroupID
                                           S GROUP ID
  #define StructuredBuffer
                                           RegularBuffer
  #define RWStructuredBuffer
                                           RW RegularBuffer
  #define ByteAddressBuffer
                                           ByteBuffer
  #define RWByteAddressBuffer
                                           RW ByteBuffer
  #define GroupMemoryBarrierWithGroupSync
                                          ThreadGroupMemoryBarrierSync
  #define groupshared
                                           thread group memory
#endif
```

#### The PS4 version



#### The PS4 version





#### The PS4 version

#### • On PS4:

- No implicit synchronization, no implicit buffer duplication You have to manage everything by yourself
- Potentially better performance because you know when you have to sync or not

#### The PS4 version

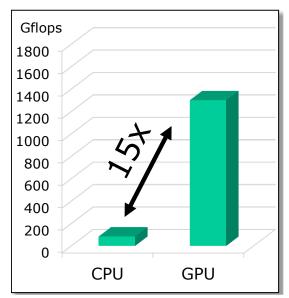
- We use labels to know if a buffer is still in use by the GPU
- Still used → Automatically allocate a new buffer
- "Used" means used by a compute shader or a copy
- We also use labels to know when a compute shader has finished, to copy the results



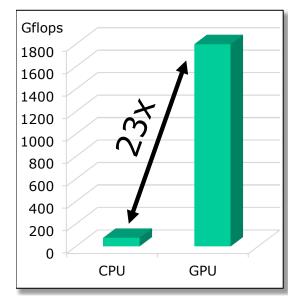
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Peak power: Xbox One



PS4





Using DirectCompute, you can do almost everything in compute shader

— The difficulty is to get good performance

Efficient code = you work on 64+ data at a time

```
if (threadIndex < 32)
{
    ...
};</pre>
```

```
if (threadIndex == 0)
{
    ...
};
```



Efficient code = you work on 64+ data at a time

```
if (threadIndex == 0)
if (threadIndex < 32)
                                                       This is likely
                                                         to be the
// Read the same data on all threads
                                                        bottleneck
. . .
```



- Example: collisions
- On the CPU:

Compute a bounding volume

(ex: Axis-Aligned Bounding Box)

Use it for an early rejection test



- Example: collisions
- On the CPU:

Compute a bounding volume (ex: Axis-Aligned Bounding Box)

Use it for an early rejection test

Use an acceleration structure (ex: AABB Tree) to improve performance



- Example: collisions
- On the GPU:

Compute a bounding volume

(ex: Axis-Aligned Bounding Box)



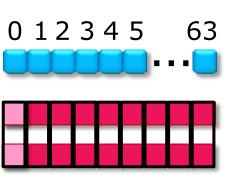


Just doing this can be more costly than computing the collision with all vertices!!!





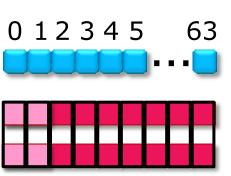
Compute 64 sub-AABoxes







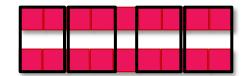
Compute 64 sub-AABoxes





- Compute 64 sub-AABoxes
- Reduce down to 32 sub-AABoxes



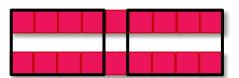


We use only 32 threads for that



- Compute 64 sub-AABoxes
- Reduce down to 32 sub-AABoxes
- Reduce down to 16 sub-AABoxes

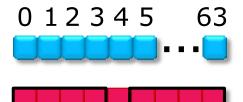




We use only 16 threads for that



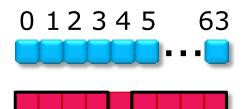
- Compute 64 sub-AABoxes
- Reduce down to 32 sub-AABoxes
- Reduce down to 16 sub-AABoxes
- Reduce down to 8 sub-AABoxes



We use only 8 threads for that



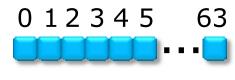
- Compute 64 sub-AABoxes
- Reduce down to 32 sub-AABoxes
- Reduce down to 16 sub-AABoxes
- Reduce down to 8 sub-AABoxes
- Reduce down to 4 sub-AABoxes

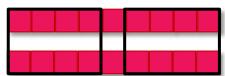


We use only 4 threads for that



- Compute 64 sub-AABoxes
- Reduce down to 32 sub-AABoxes
- Reduce down to 16 sub-AABoxes
- Reduce down to 8 sub-AABoxes
- Reduce down to 4 sub-AABoxes
- Reduce down to 2 sub-AABoxes

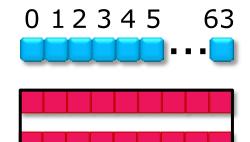




We use only 2 threads for that



- Compute 64 sub-AABoxes
- Reduce down to 32 sub-AABoxes
- Reduce down to 16 sub-AABoxes
- Reduce down to 8 sub-AABoxes
- Reduce down to 4 sub-AABoxes
- Reduce down to 2 sub-AABoxes
- Reduce down to 1 AABox—



We use a single thread for that



- Compute 64 sub-AABoxes
- Reduce down to 32 sub-AABoxes
- Reduce down to 16 sub-AABoxes
- Reduce down to 8 sub-AABoxes
- Reduce down to 4 sub-AABoxes
- Reduce down to 2 sub-AABoxes
- Reduce down to 1 AABox

This is ~ as costly as computing the collision with 7 x 64 = 448 vertices!!



Atomic functions are available



You can write lock-free thread-safe containers

Too costly in practice



Atomic functions are available



You can write lock-free thread-safe containers

Too costly in practice



The brute-force approach is almost always the fastest one

Atomic functions are available



You can write lock-free thread-safe containers

Too costly in practice



The brute-force approach is almost always the fastest one

- Bandwidth usage
- Data compression
- Memory coalescing
- LDS usage



Port an algorithm to the GPU only if you find a way to handle 64+ data at a time 95+% of the time



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## Sharing code between C++ & hlsl

```
#if defined(_WIN32) || defined(_WIN64)
 || defined(_DURANGO) || defined(__ORBIS__)
   typedef unsigned long uint
   struct float2 { float x, y;
   struct float3 { float x, y, z; };
   struct float4 { float x, y, z, w; };
   struct uint2 { uint x, y;
   struct uint3 { uint x, y, w; };
   struct uint4 { uint x, y, z, w; };
#endif
```



# Debug buffer

```
struct DebugBuffer
{
...
};
```



#### Debug buffer

```
struct DebugBuffer
{
   float3 m_Velocity;
   float m_Weight;
};
```

```
// Uncomment the following line
// to use the debug buffer
#define USE_DEBUG_BUFFER
```

```
#ifdef USE_DEBUG_BUFFER
RWStructuredBuffer<DebugBuffer> g_DebugBuffer : register(u1);
#endif
```



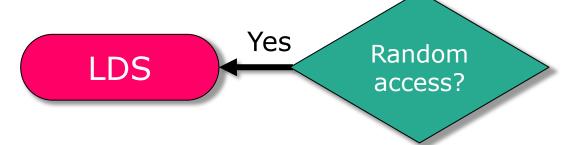
#### Debug buffer

```
struct DebugBuffer
                         // Uncomment the following line
                         // to use the debug buffer
 float3 m_Velocity;
                         #define USE DEBUG BUFFER
 float m_Weight;
#ifdef USE_DEBUG_BUFFER
  RWStructuredBuffer<DebugBuffer> g_DebugBuffer: register(u1);
#endif
```

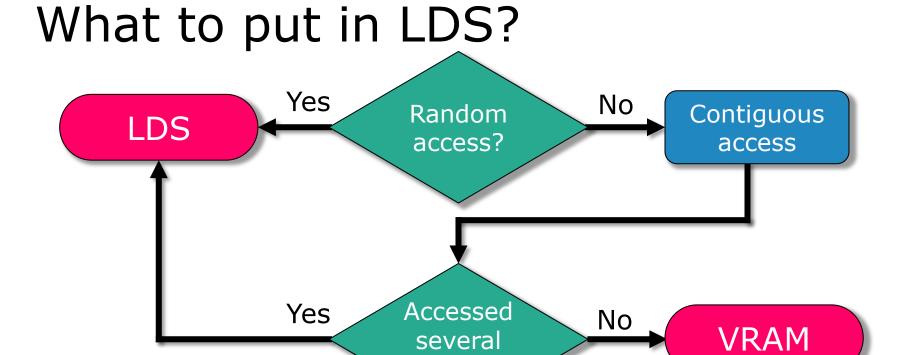
WRITE\_IN\_DEBUG\_BUFFER(m\_Velocity, threadIndex, value);

DebugBuffer \*debugBuffer = GetDebugBuffer();









times?

#### GDCEUROPE.COM

#### Memory consumption in LDS

• LDS = 64 KB per compute unit

1 thread group can access 32 KB

• LDS = 64 KB per compute unit



1 thread group can access 32 KB





2 thread groups can run simultaneously on the same compute unit



• LDS = 64 KB per compute unit

1 thread group can access 32 KB





2 thread groups can run simultaneously on the same compute unit

- Less memory used in LDS
  - 9

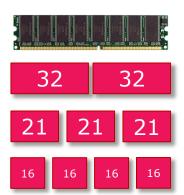
More thread groups can run in parallel



- LDS = 64 KB per compute unit
- 1 thread group can access 32 KB



2 thread groups can run simultaneously on the same compute unit



Less memory used in LDS



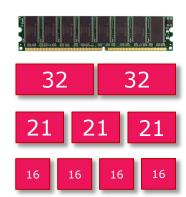
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- LDS = 64 KB per compute unit
- 1 thread group can access 32 KB



2 thread groups can run simultaneously on the same compute unit



Less memory used in LDS



More thread groups can run in parallel

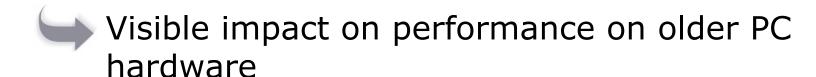
#### Optimizing bank access in LDS?

- LDS is divided into several banks (16 or 32)
- 2 threads accessing the same bank → Conflict



### Optimizing bank access in LDS?

- LDS is divided into several banks (16 or 32)
- 2 threads accessing the same bank → Conflict







```
CopyFromVRAMToLDS(););
ReadInputFromLDS(););
DoSomeComputations());
WriteOutputToLDS():):
ReadInputFromLDS()();
DoSomeComputations(););
WriteOutputToLDS(););
CopyFromLDSToVRAM(););
```





```
CopyFromVRAMToLDS();
ReadInputFromLDS();
DoSomeComputations();
WriteOutputToLDS():):
ReadInputFromLDS()();
DoSomeComputations(););
WriteOutputToLDS(););
//CopyFromLDSToVRAM();
```



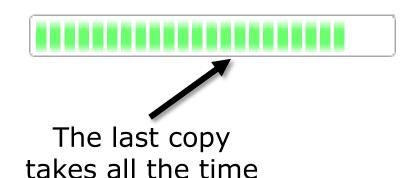


CopyFromVRAMToLDS();
ReadInputFromLDS();

DoSomeComputations();
WriteOutputToLDS();

ReadInputFromLDS();
DoSomeComputations();
WriteOutputToLDS();

CopyFromLDSToVRAM();



This doesn't make sense!



```
CopyFromVRAMToLDS();
ReadInputFromLDS();
DoSomeComputations();
WriteOutputToLDS():
ReadInputFromLDS();
DoSomeComputations();
WriteOutputToLDS();
//CopyFromLDSToVRAM();
```



### Optimizing compilation time

```
float3 fanBlades[10];
for (uint i = 0; i < 10; ++i)
  Vertex fanVertex = GetVertexInLDS(neighborFan.m VertexIndex[i]);
  fanBlades[i] = fanVertex.m Position - fanCenter.m Position;
float3 normalAccumulator = cross(fanBlades[0], fanBlades[1]);
for (uint i = 0; i < 8; ++i)
    float3 triangleNormal = cross(fanBlades[j+1], fanBlades[j+2]);
     uint isTriangleFilled = neighborFan.m_FilledFlags & (1 << j);
    if (isTriangleFilled) normalAccumulator += triangleNormal;
```

## Optimizing compilation time

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```



### Optimizing compilation time

```
float3 fanBlades[10];
                                   Shader compilation time
for (uint i = 0; i < 10; ++i)
                                                              19"
                                         Loop
  Vertex fanVertex = GetVerte
                                 Manually unrolled
  fanBlades[i] = fanVertex.m
float3 normalAccumulator = cross(fanBlades[0], fanBlades[1]);
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    if (isTriangleFilled) normalAccumulator += triangleNormal;
```

#### Iteration time

- It's really hard to know which code will run the fastest.
- The "best" method:
  - Write 10 versions of your feature.
  - Test them.
  - Keep the fastest one.

# Iteration time

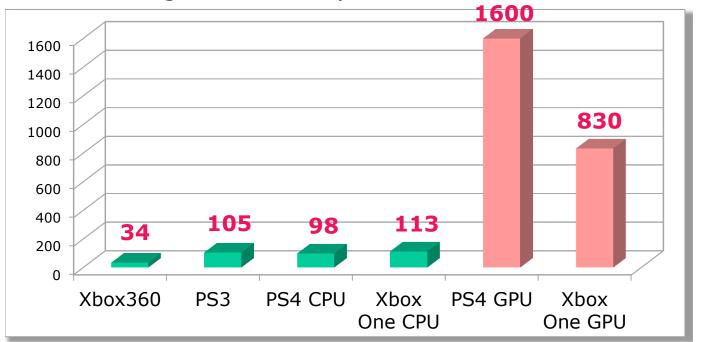
- It's really hard to know which code will run the fastest.
- The "best" method:
  - Write 10 versions of your feature.
  - Test them.
  - Keep the fastest one.
- A fast iteration time really helps



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#### Bonus: final performance

Next gen can be sexy after all!



#### PS4 – 2 ms of GPU time – 640 dancers



# Thank you!

#### Questions?



